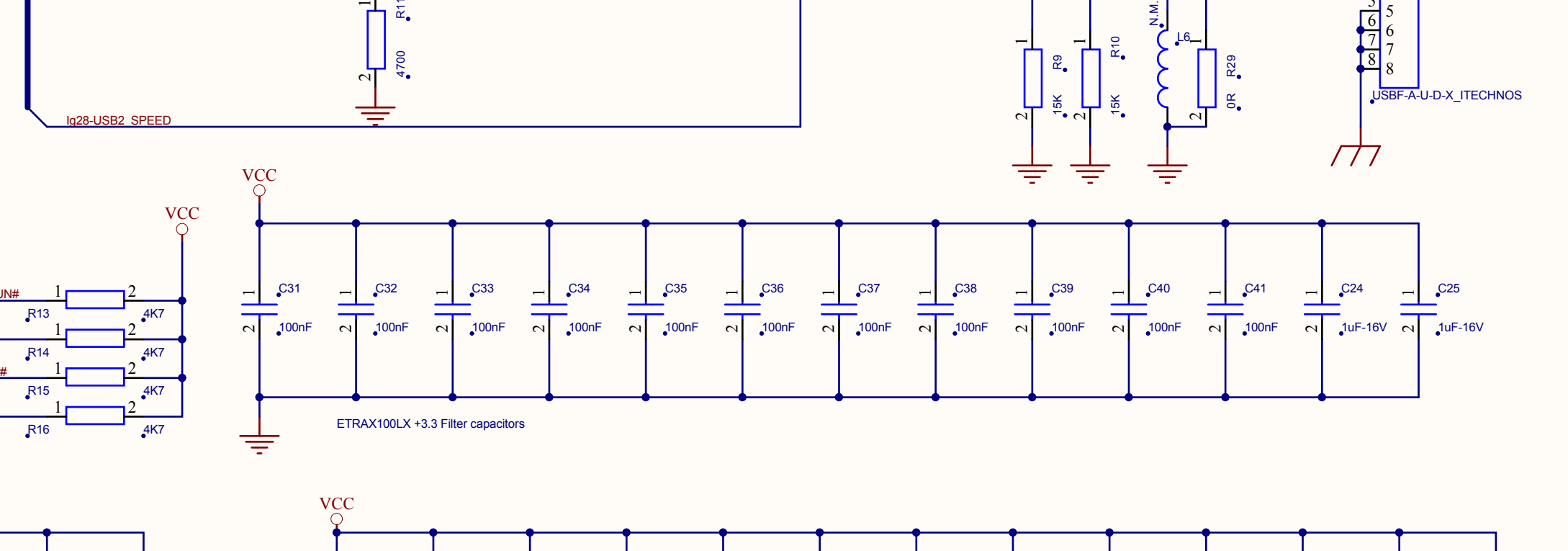
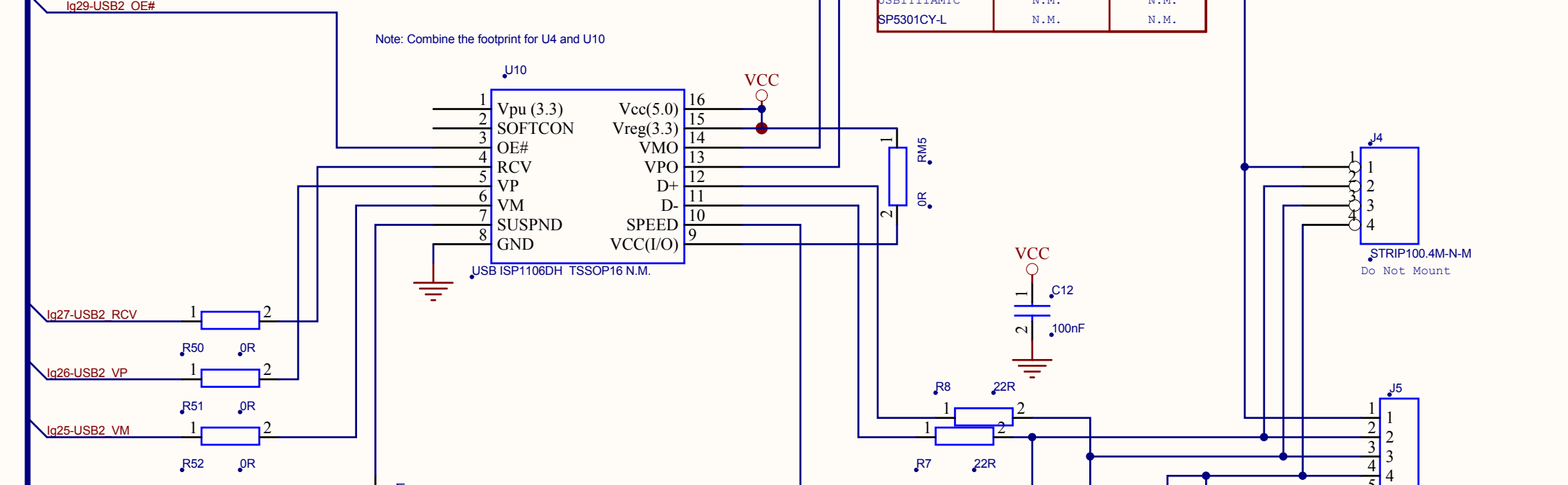
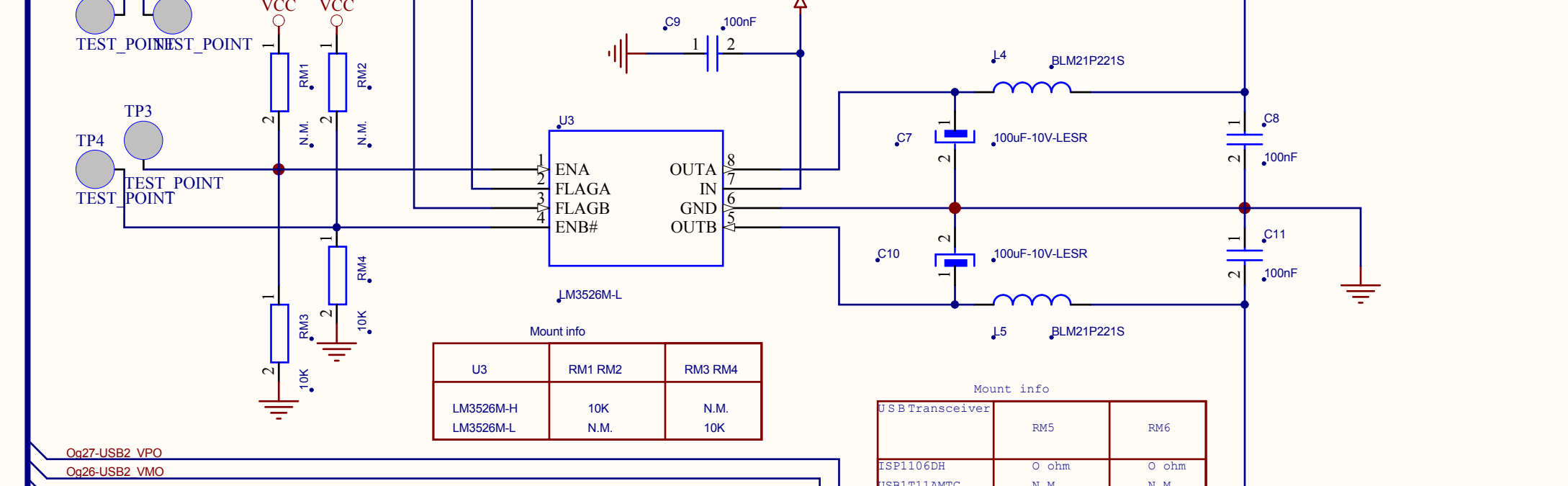
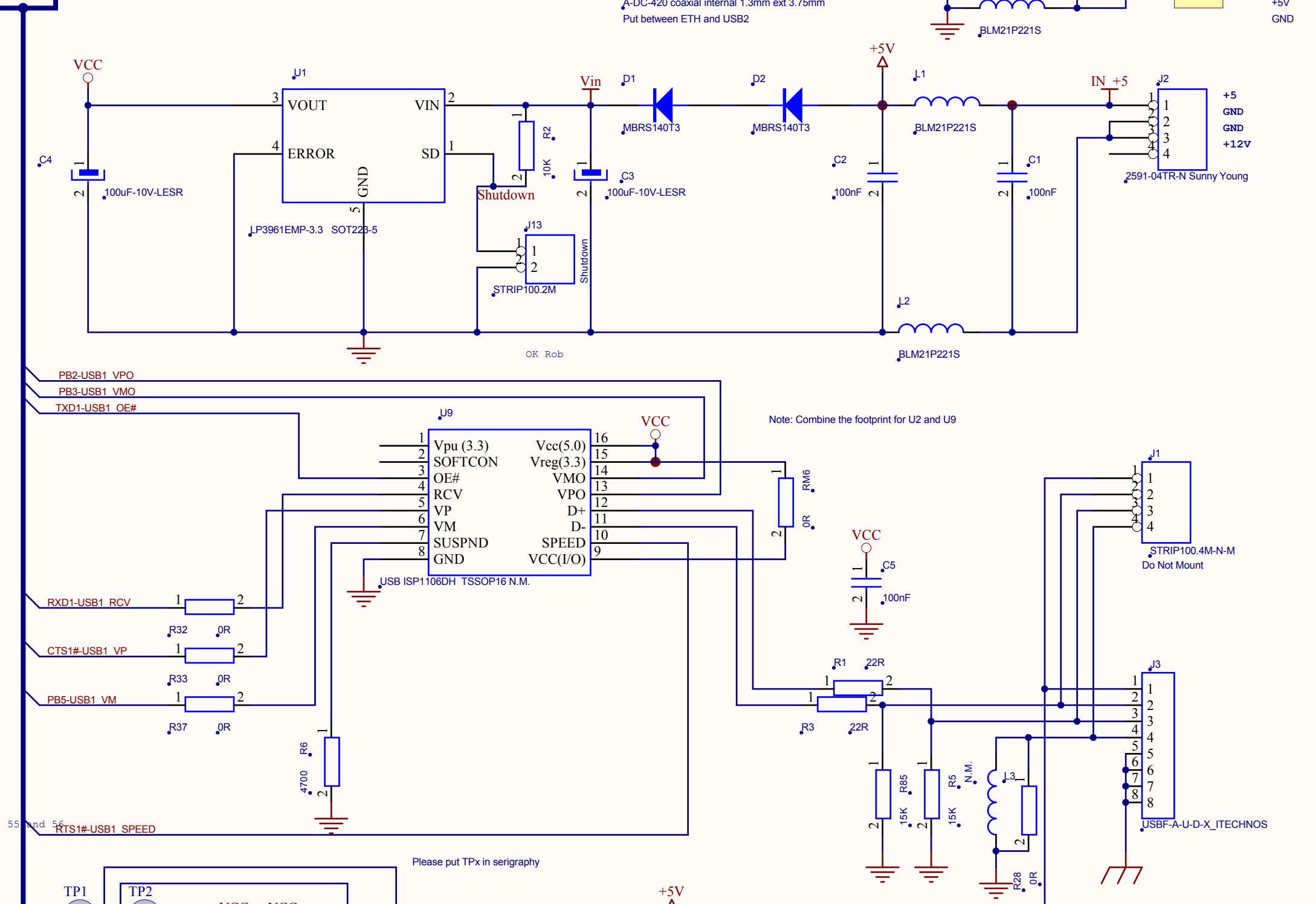
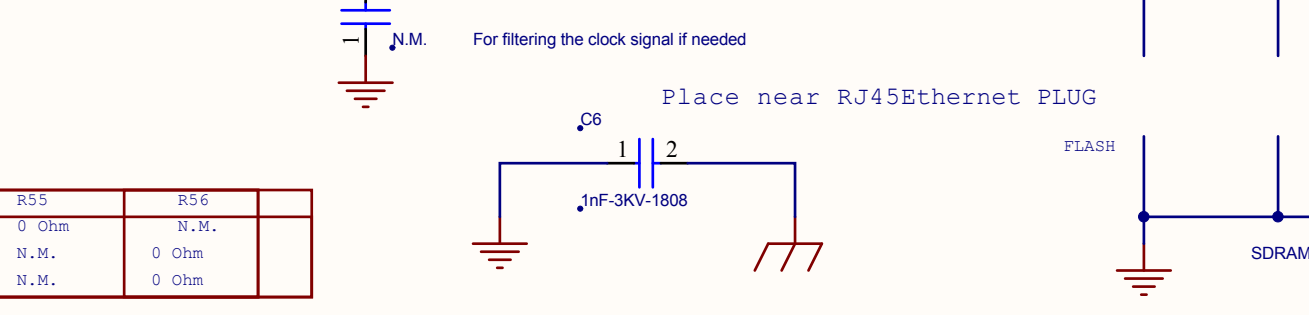
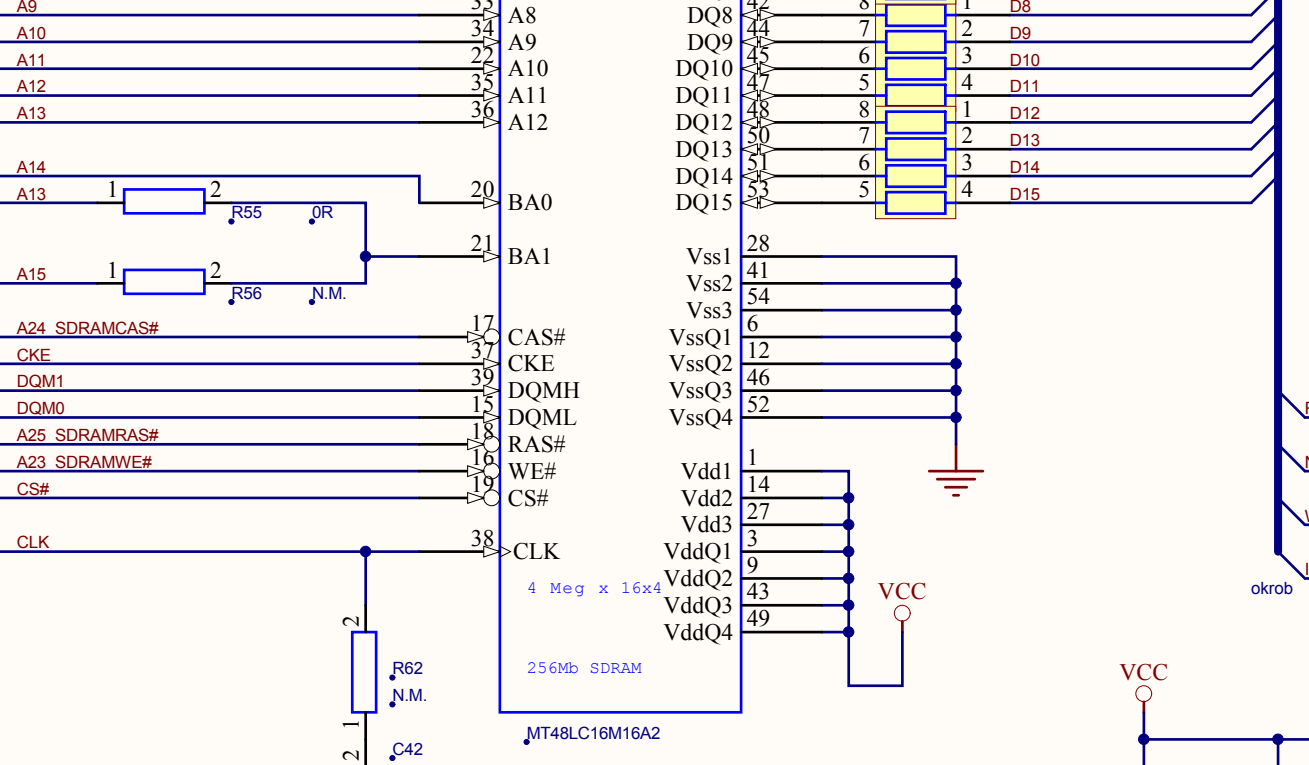
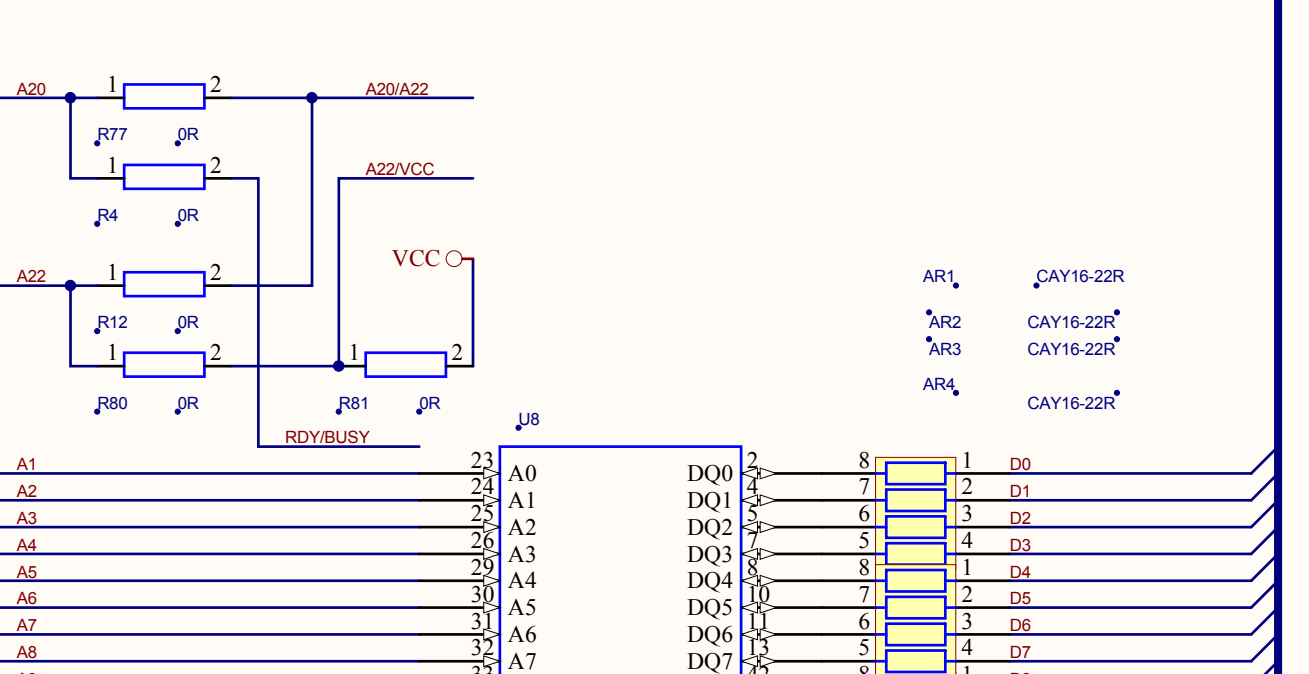
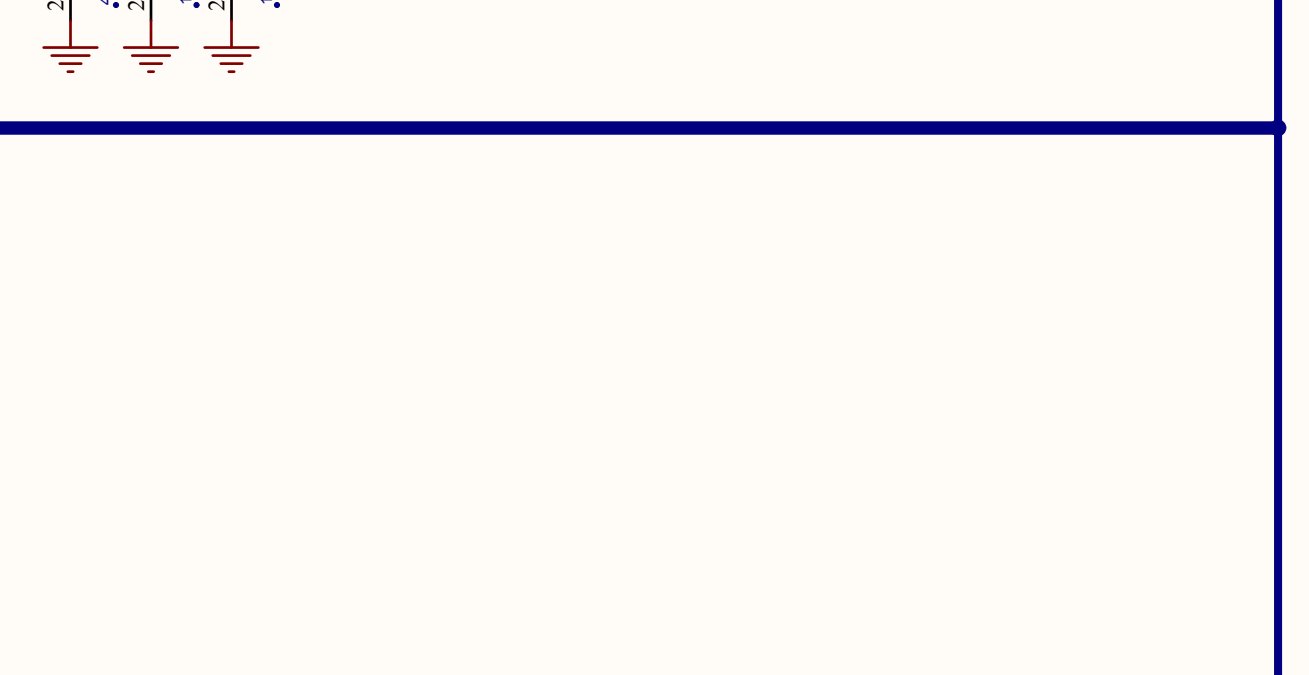
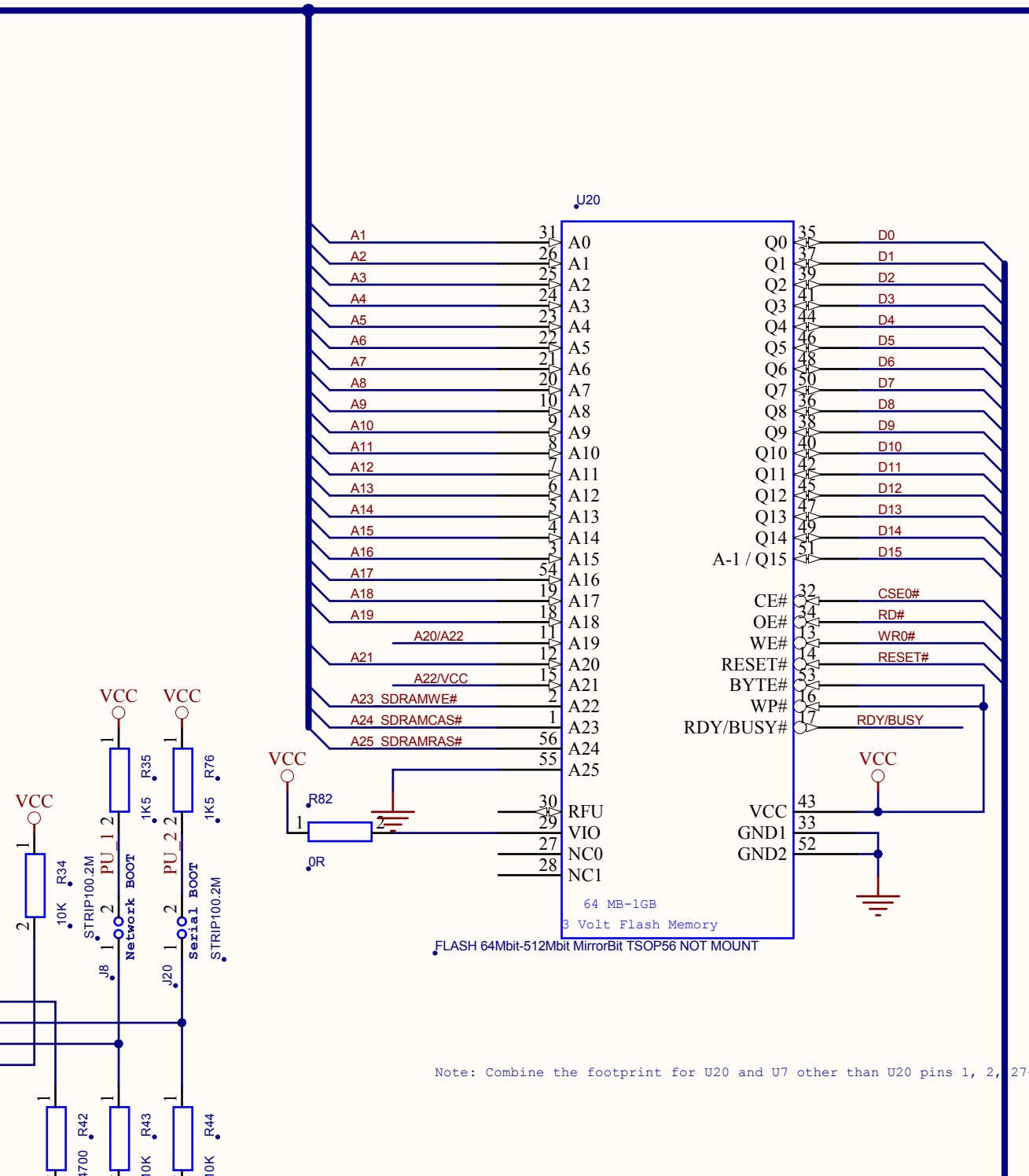
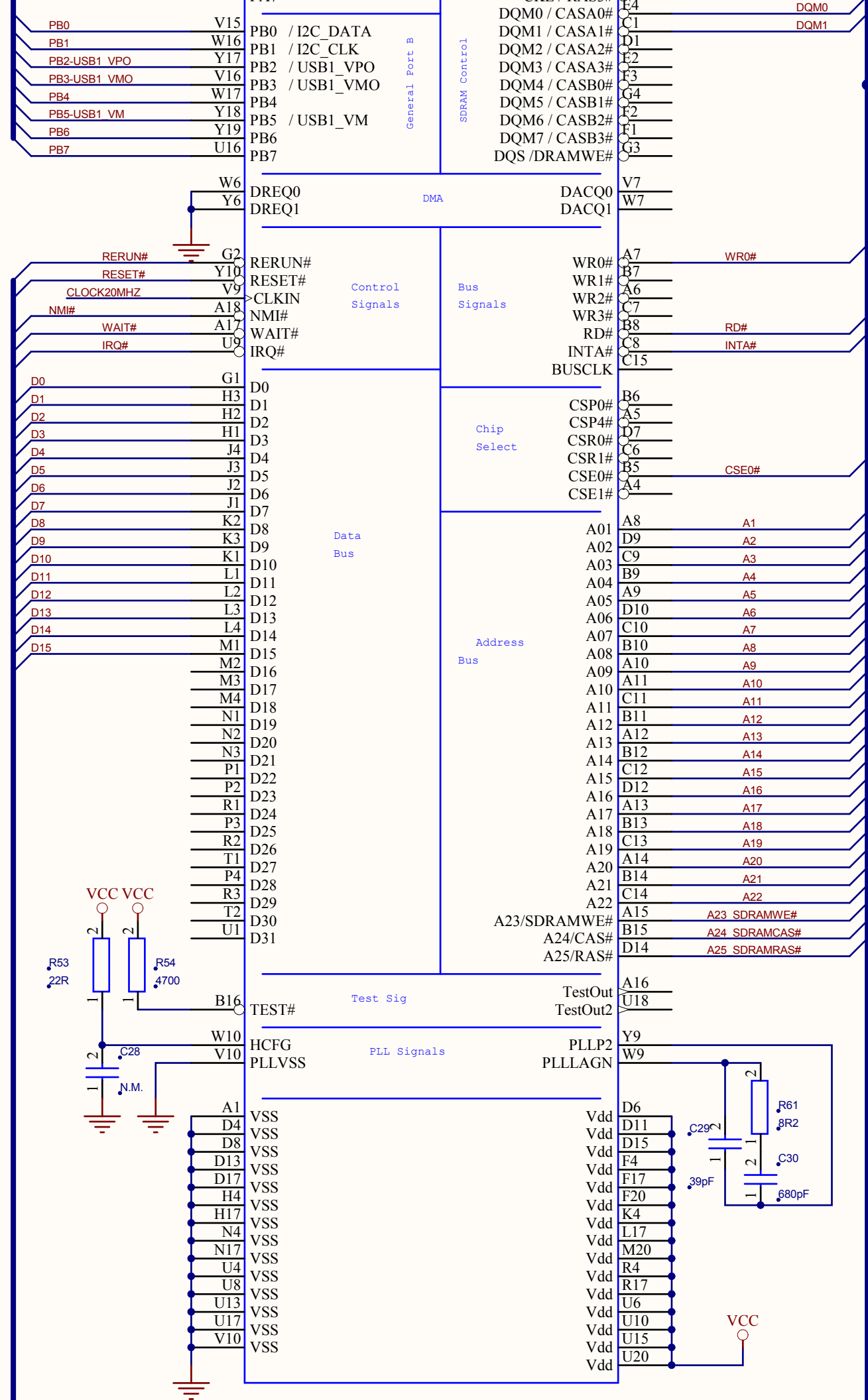
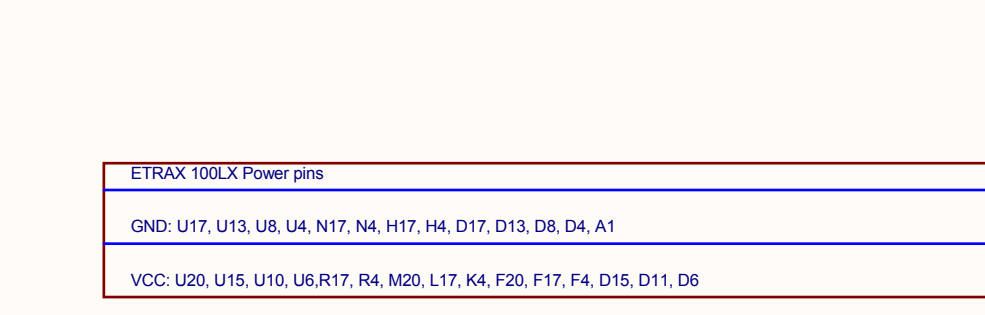
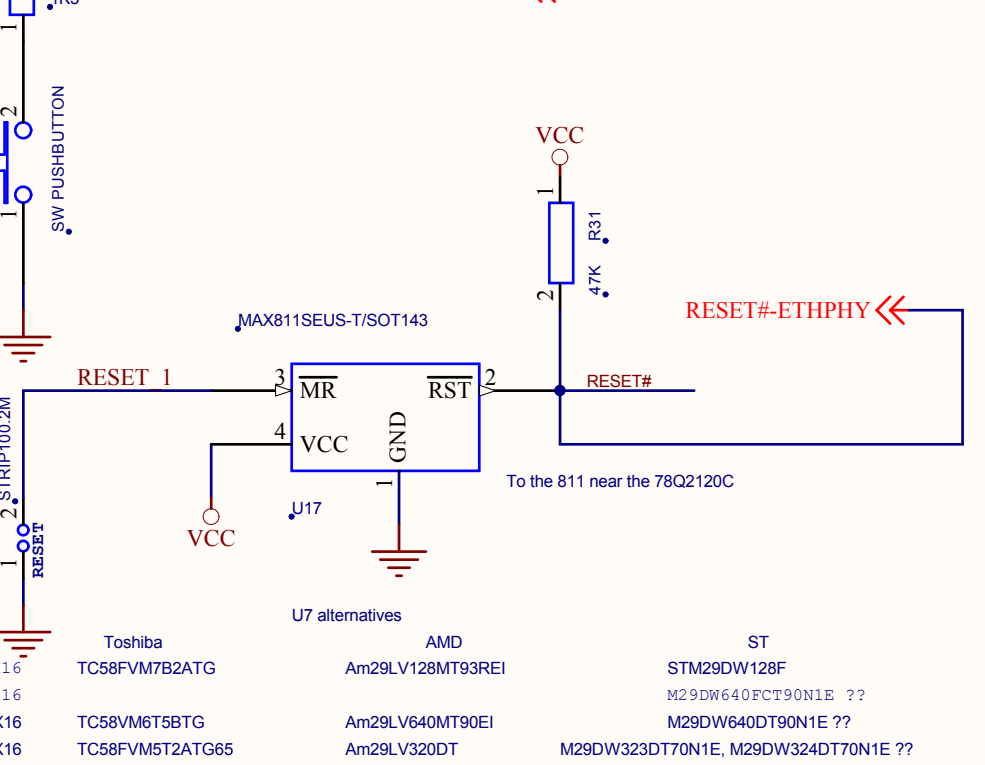
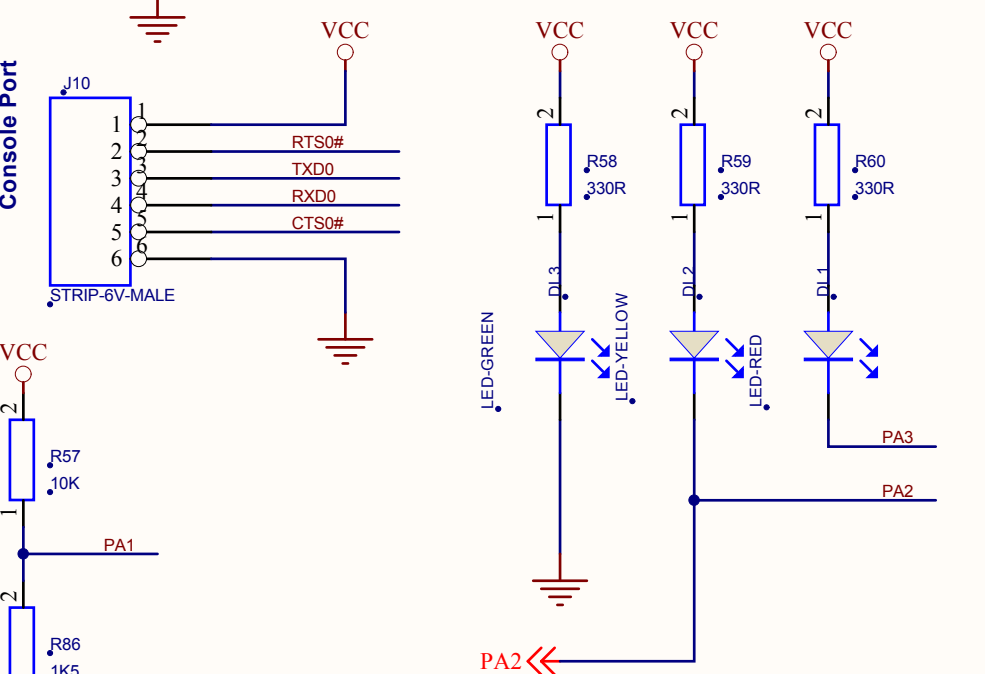
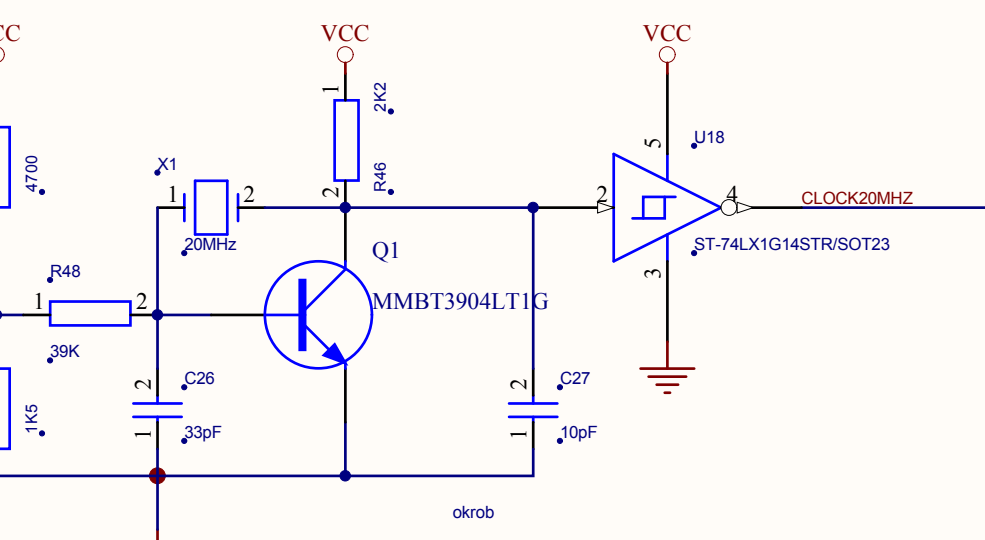
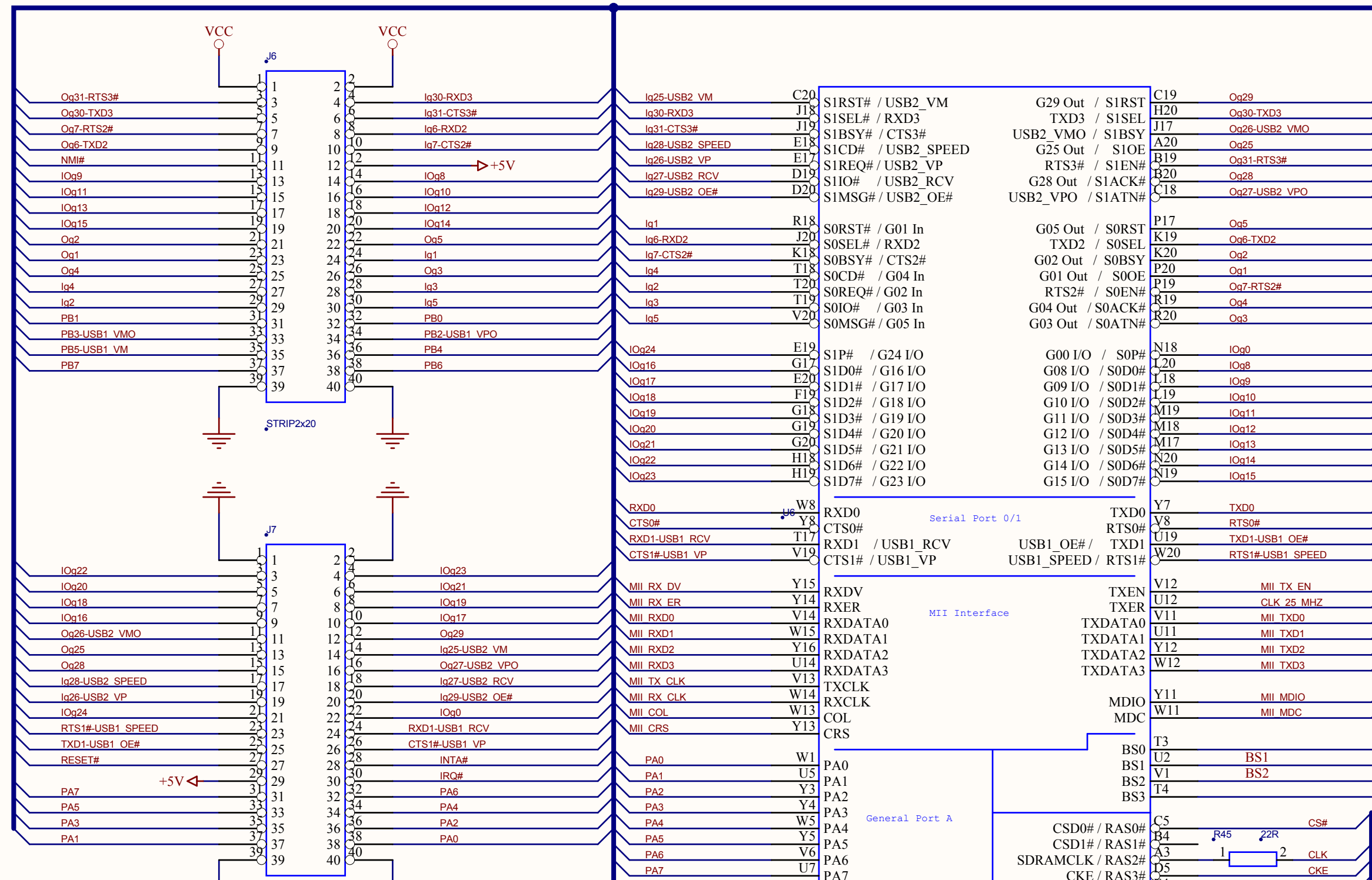
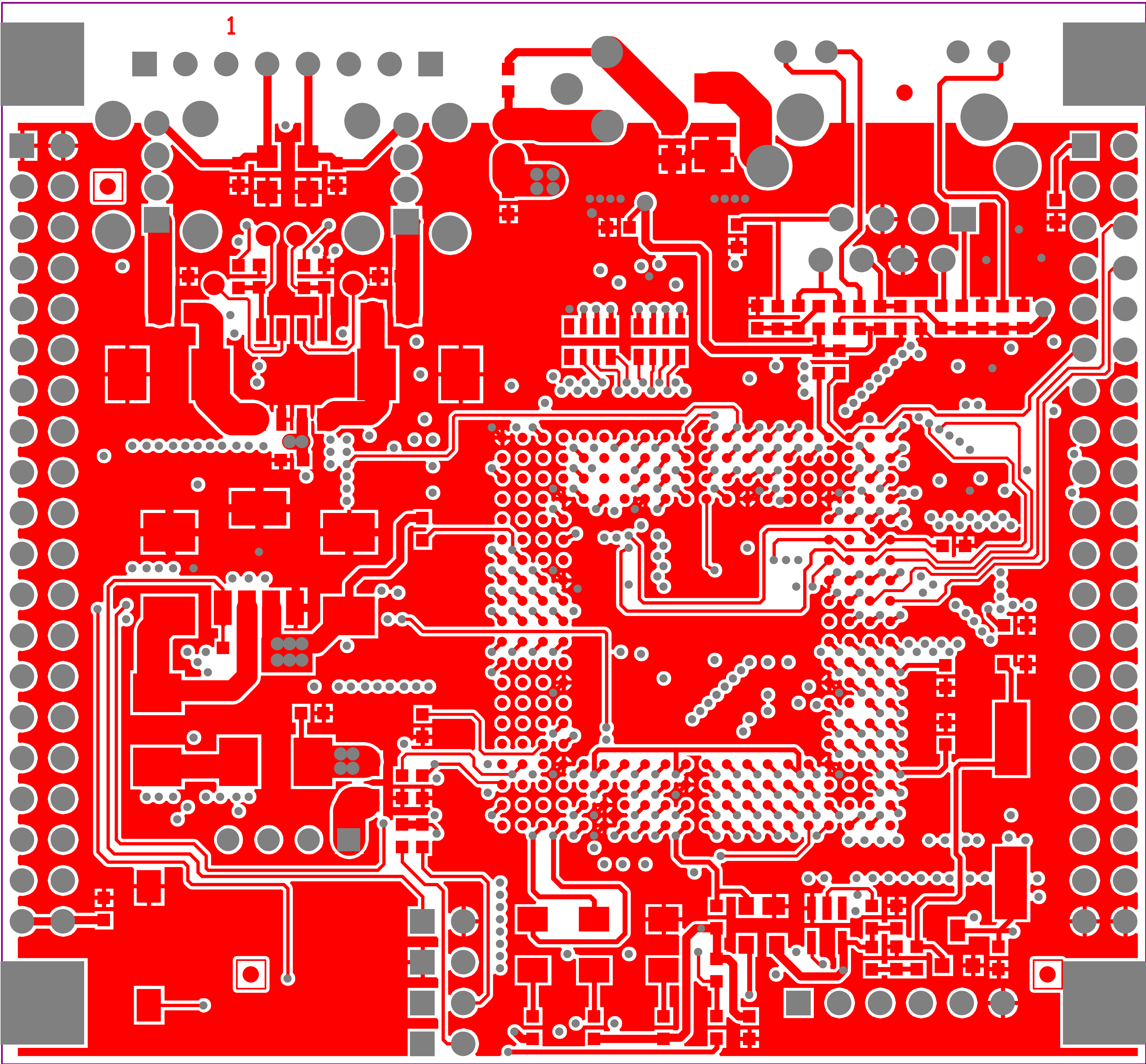




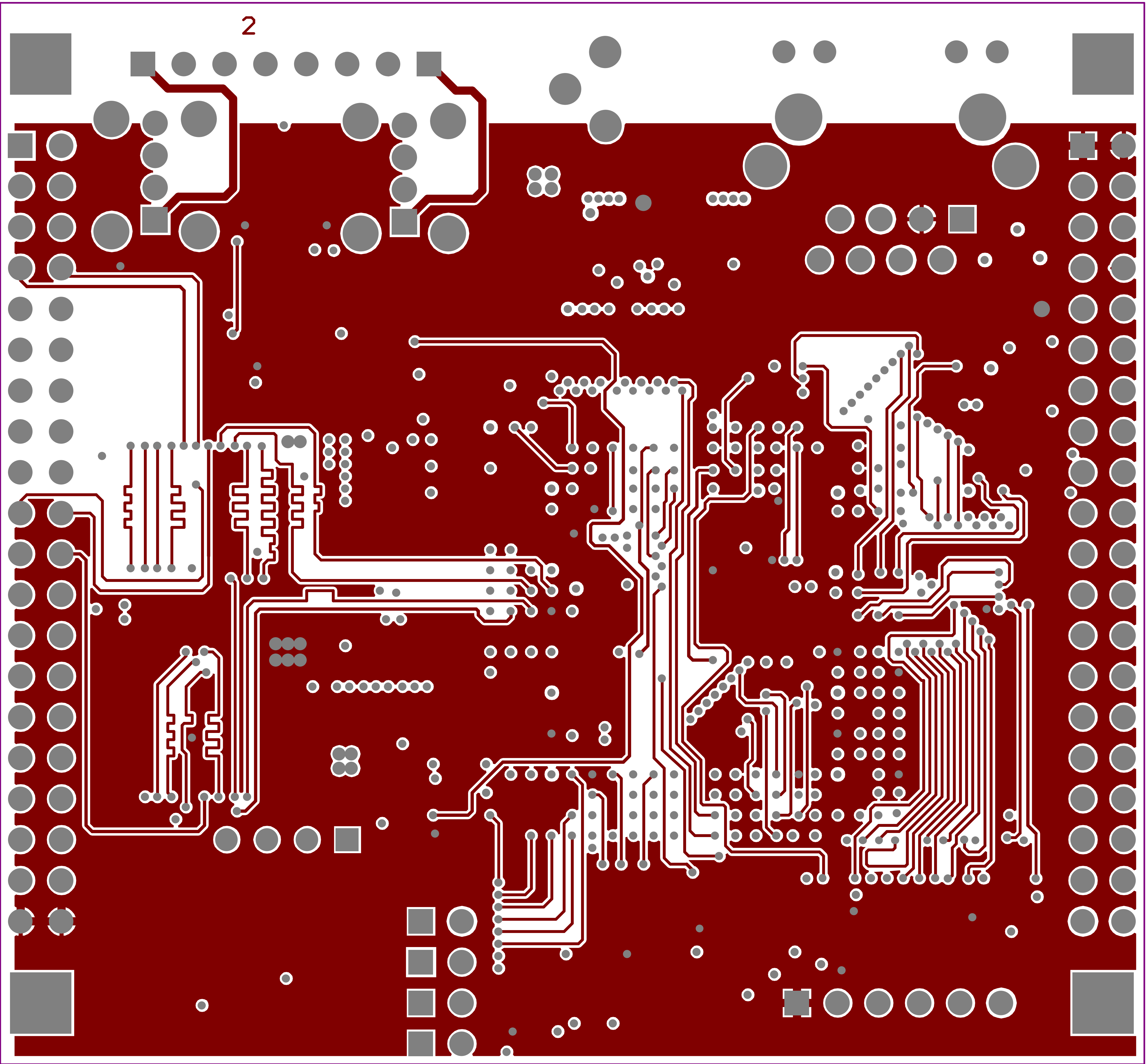
Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Top Layer

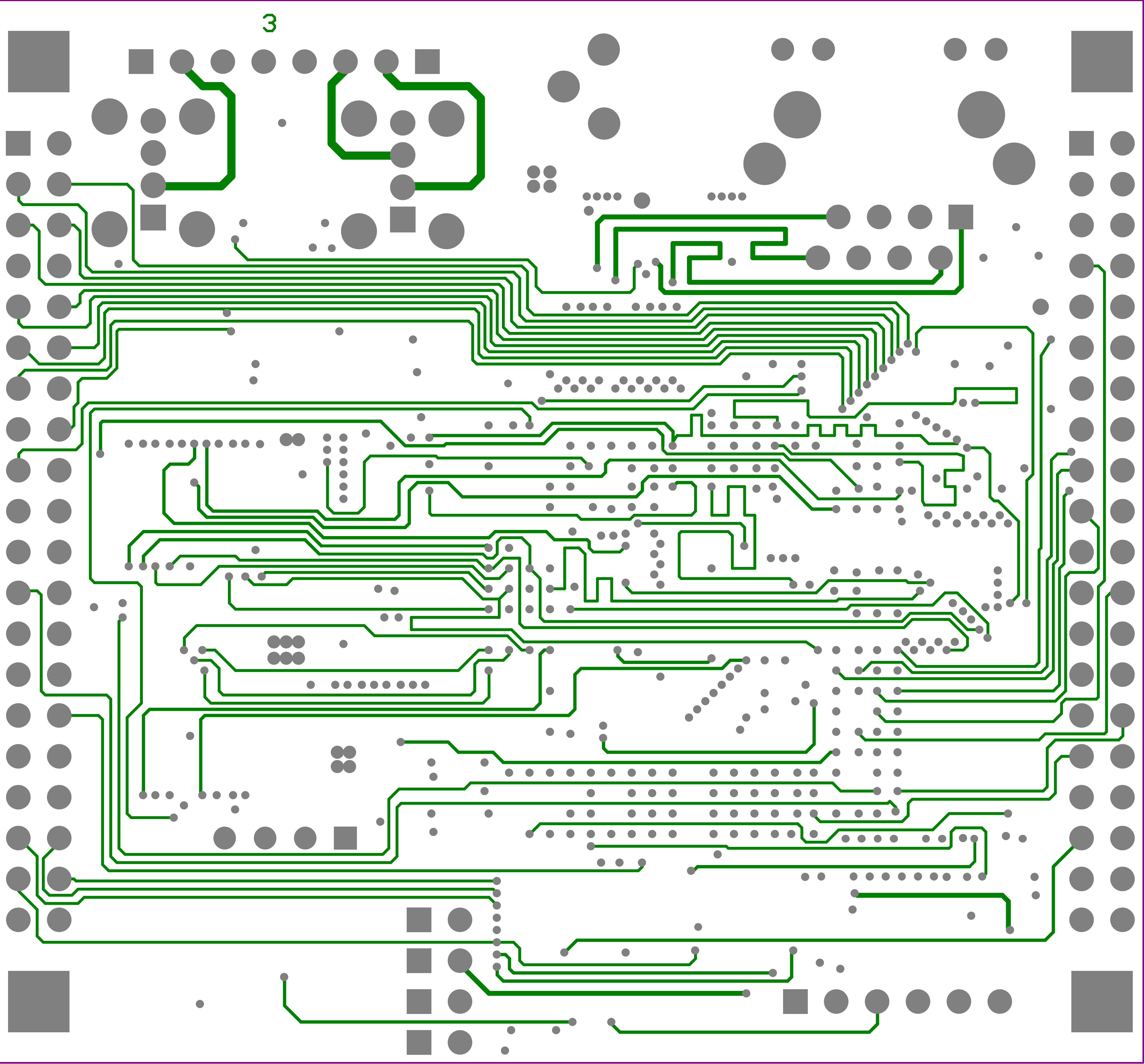
Mechanical 1

Multi Layer



Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer Name
MidLayer1
Mechanical 1
Multi Layer



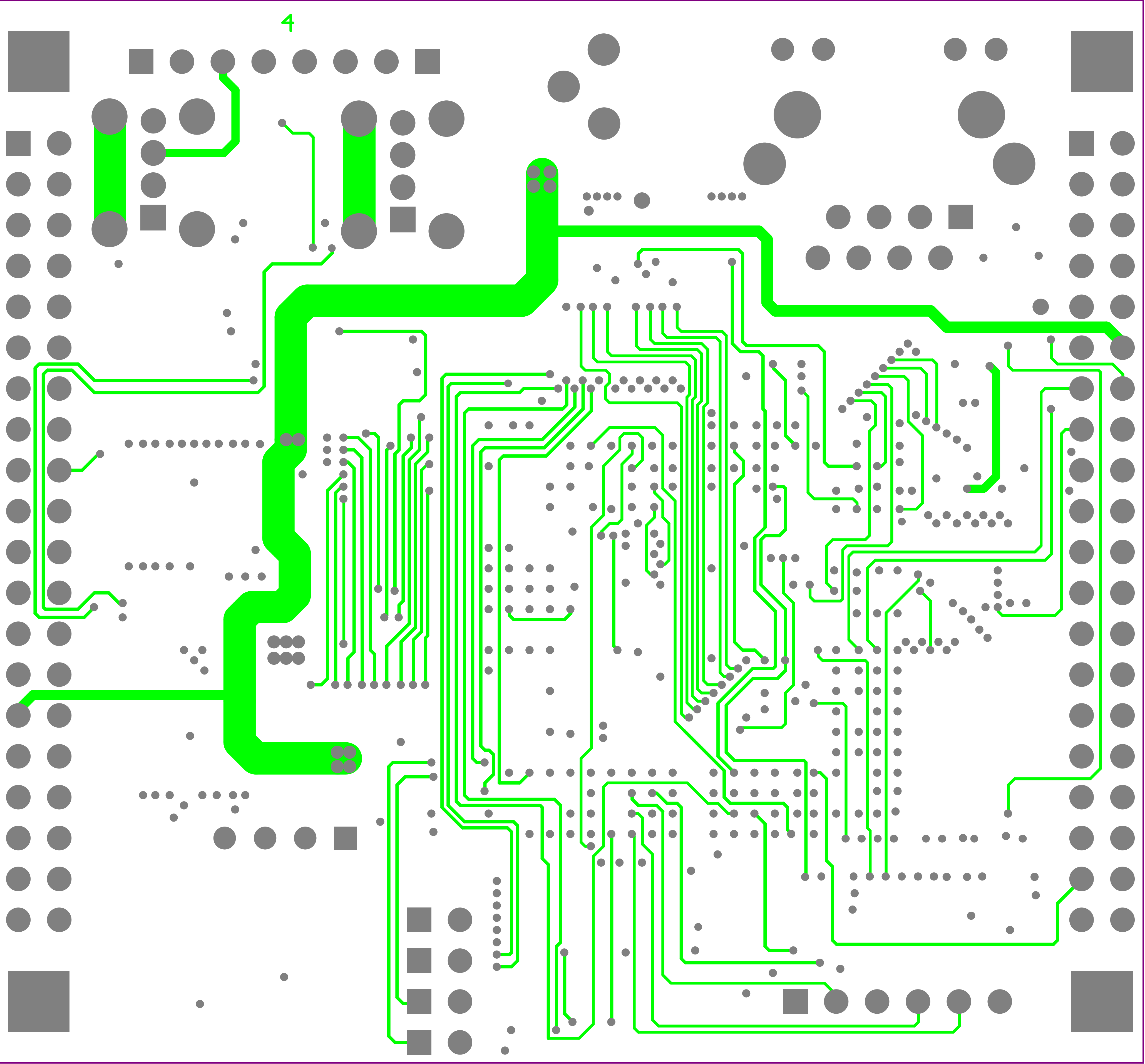
Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

MidLayer2

Mechanical 1

Multi Layer



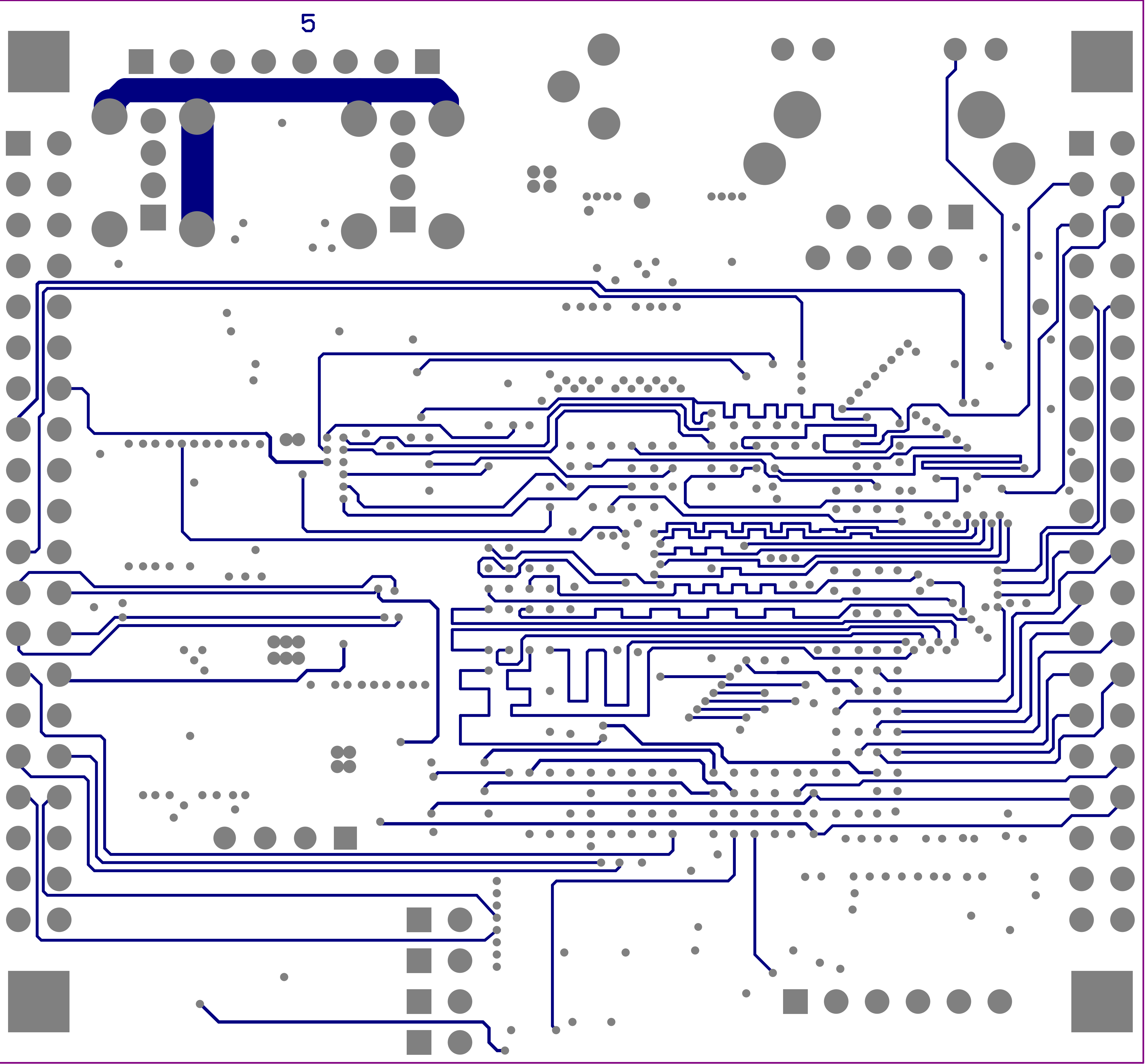
Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

MidLayer3

Mechanical 1

Multi Layer



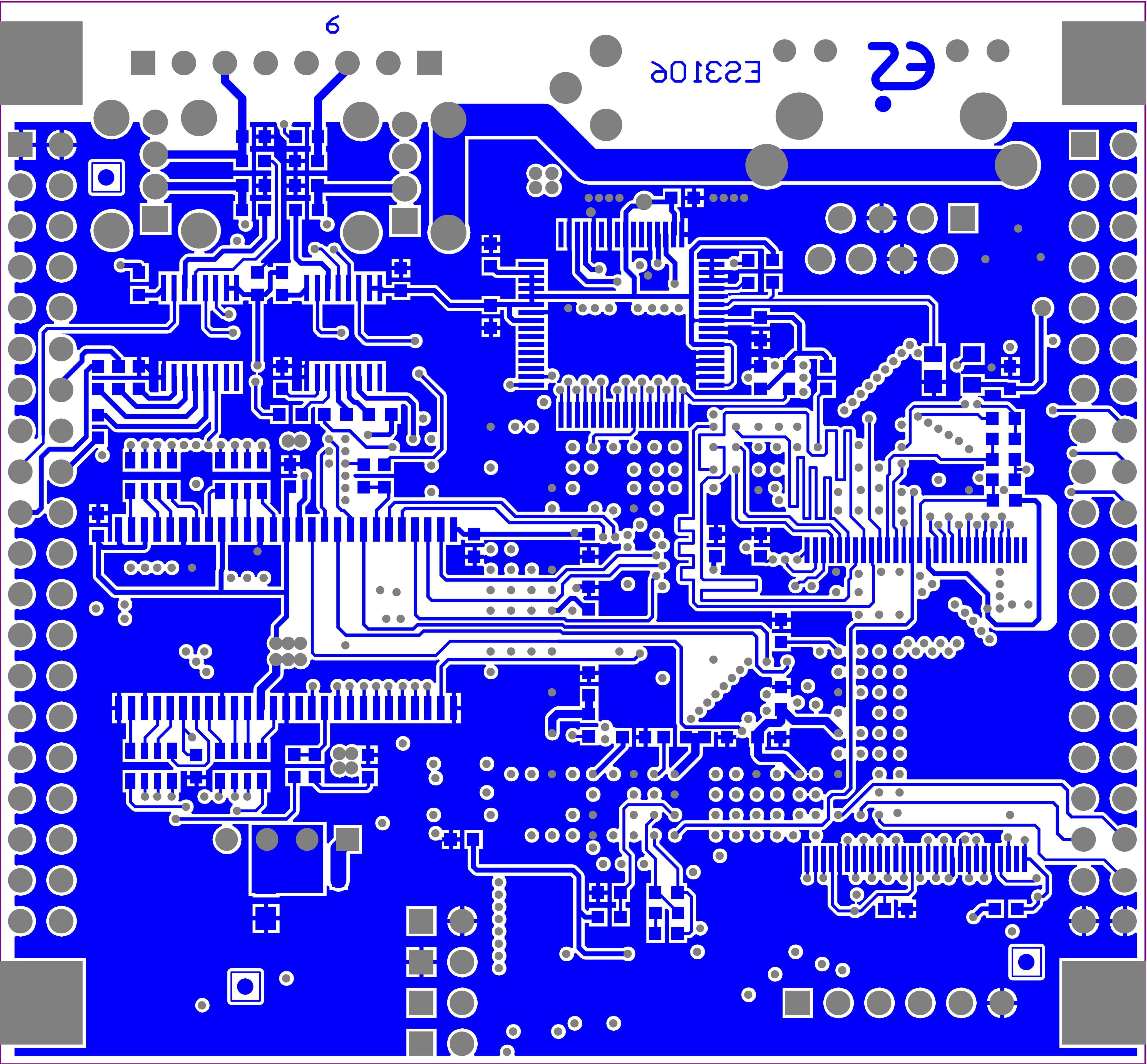
Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

MidLayer 4

Mechanical 1

Multi Layer

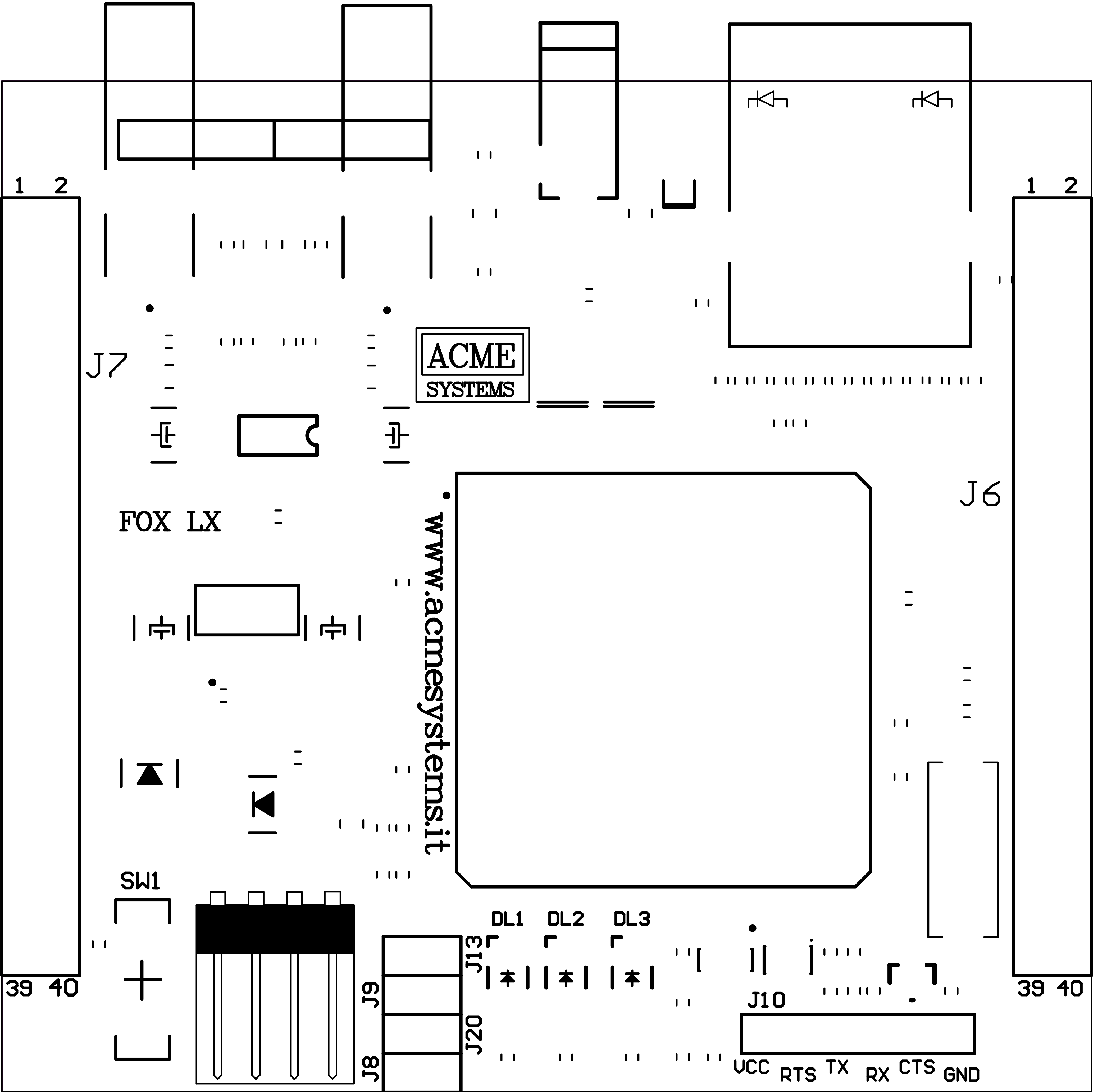


Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Bottom Layer
Mechanical 1

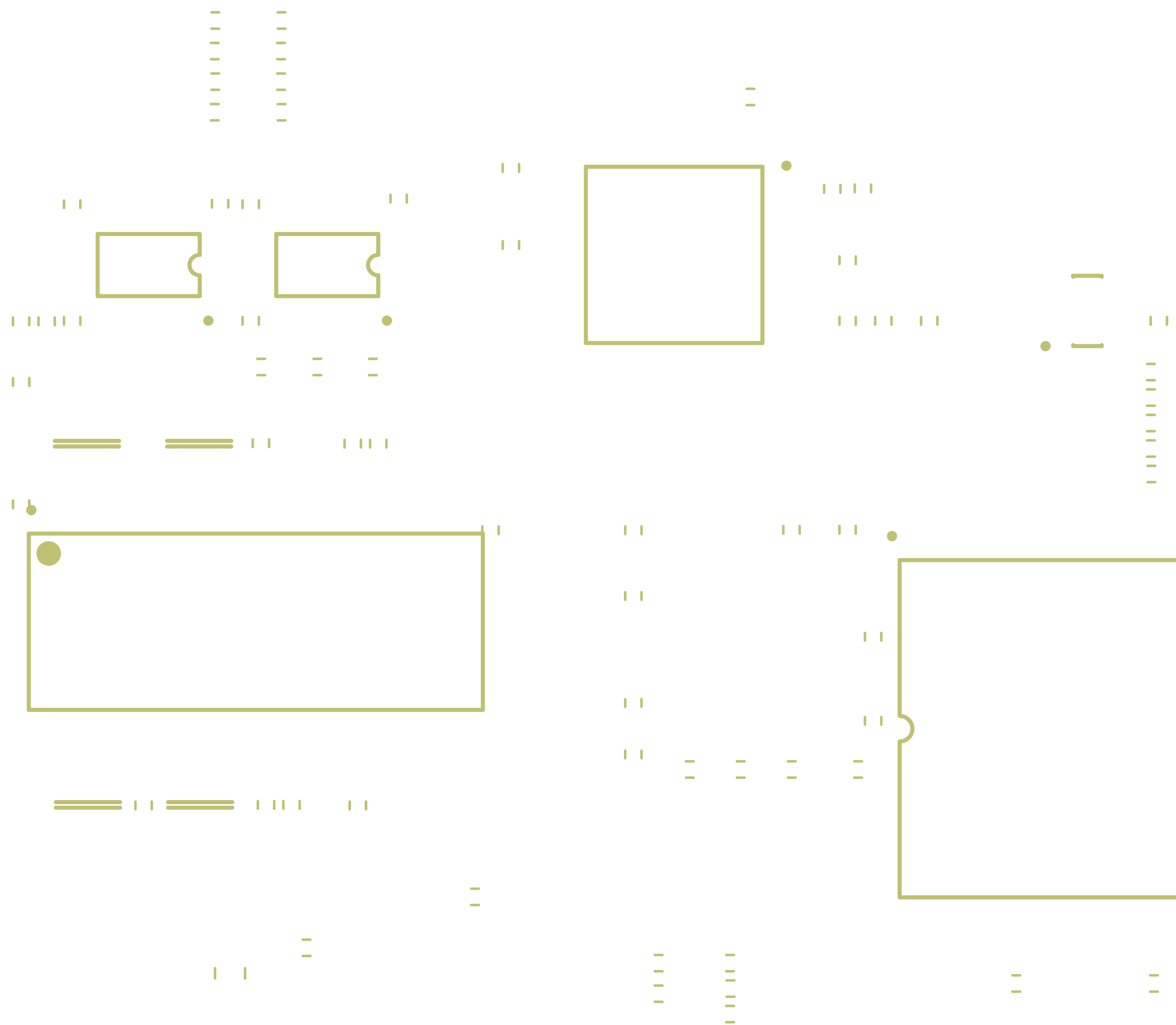
Multi Layer



Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Mechanical 1
Top Overlay

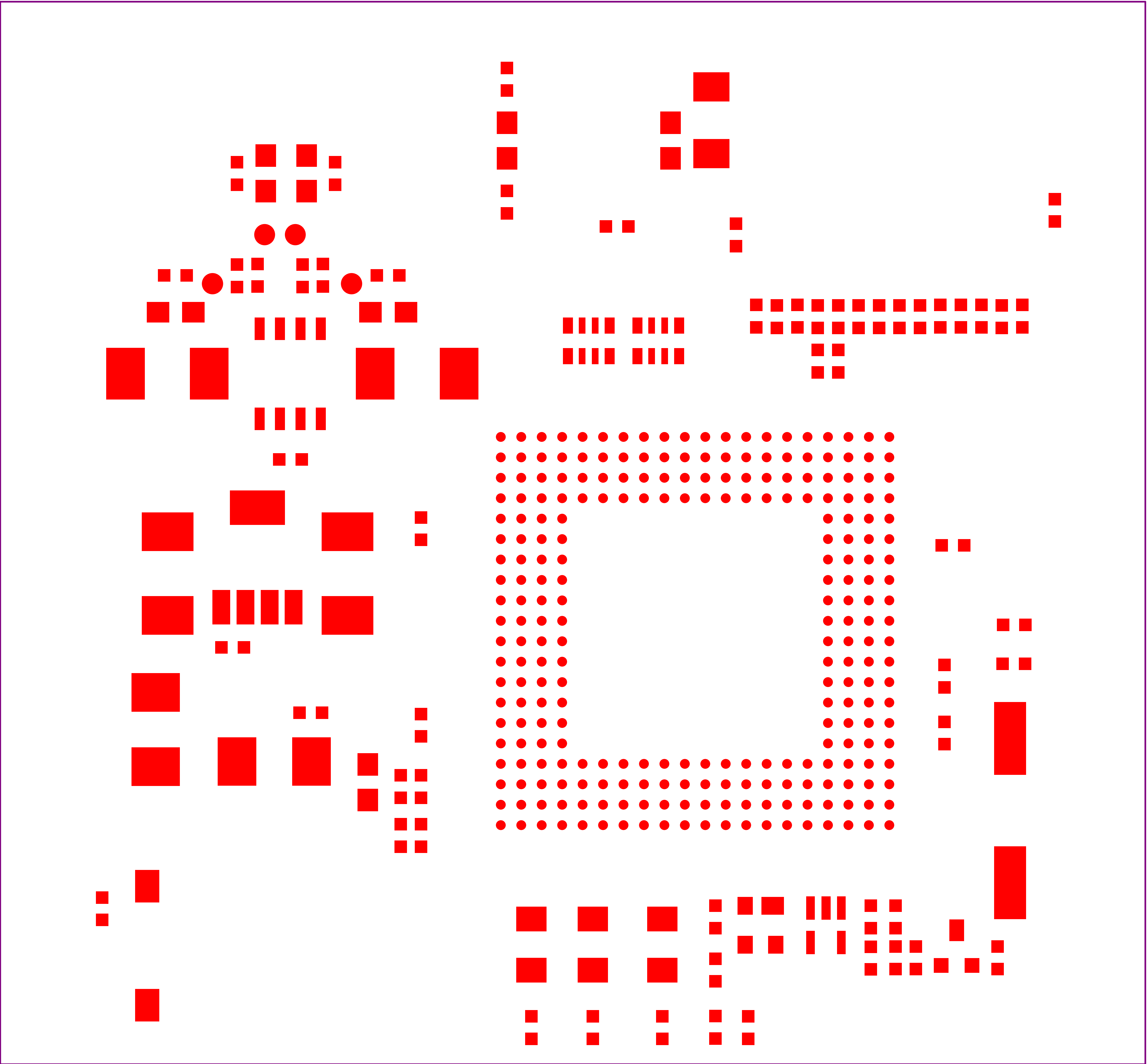


Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer	Name
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Mechanical 1

Bottom Overlay

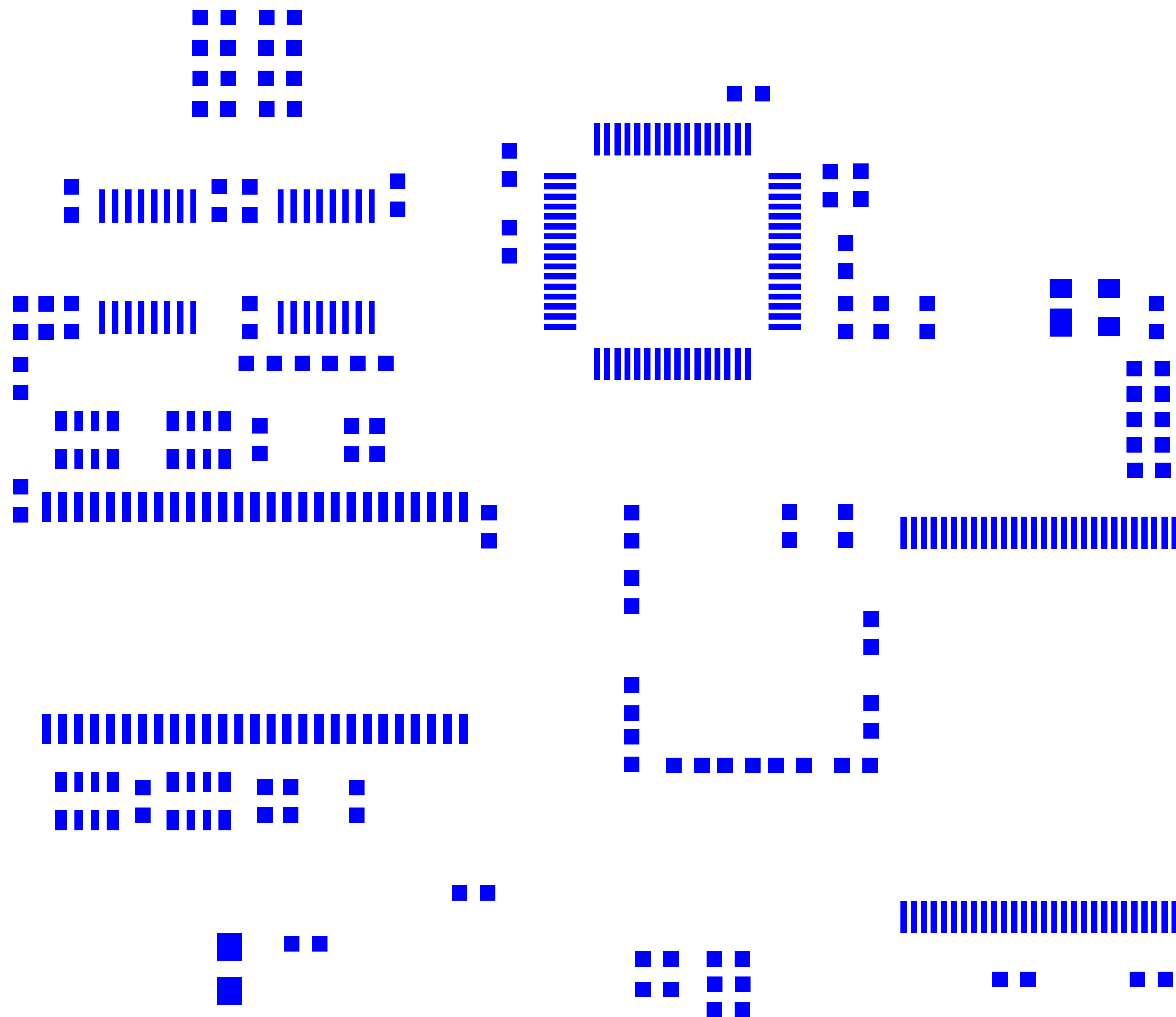


Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Mechanical 1

Top Paste

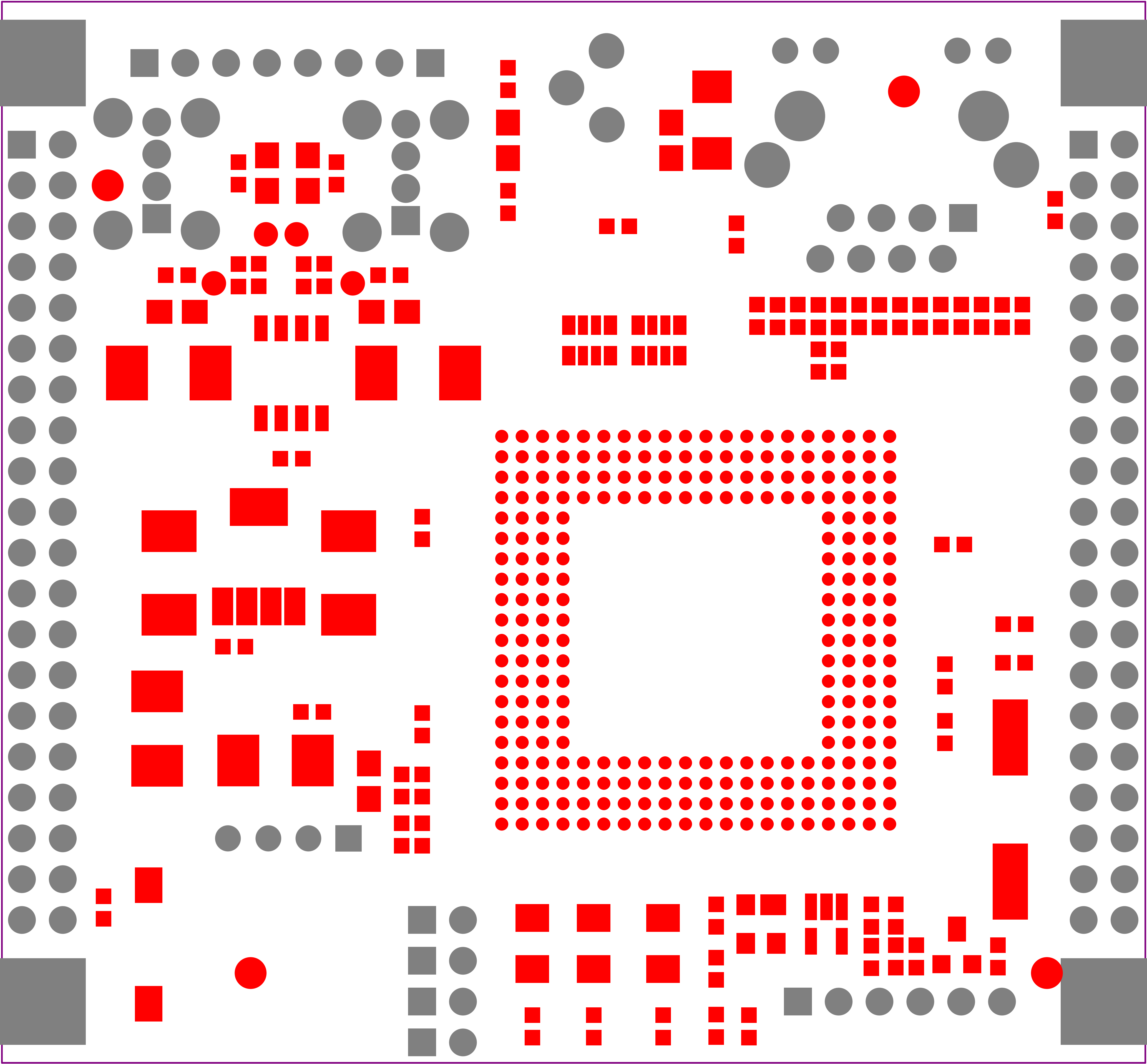


Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer	Name
1	Input
2	Hidden
3	Output

Mechanical 1

Bottom Paste

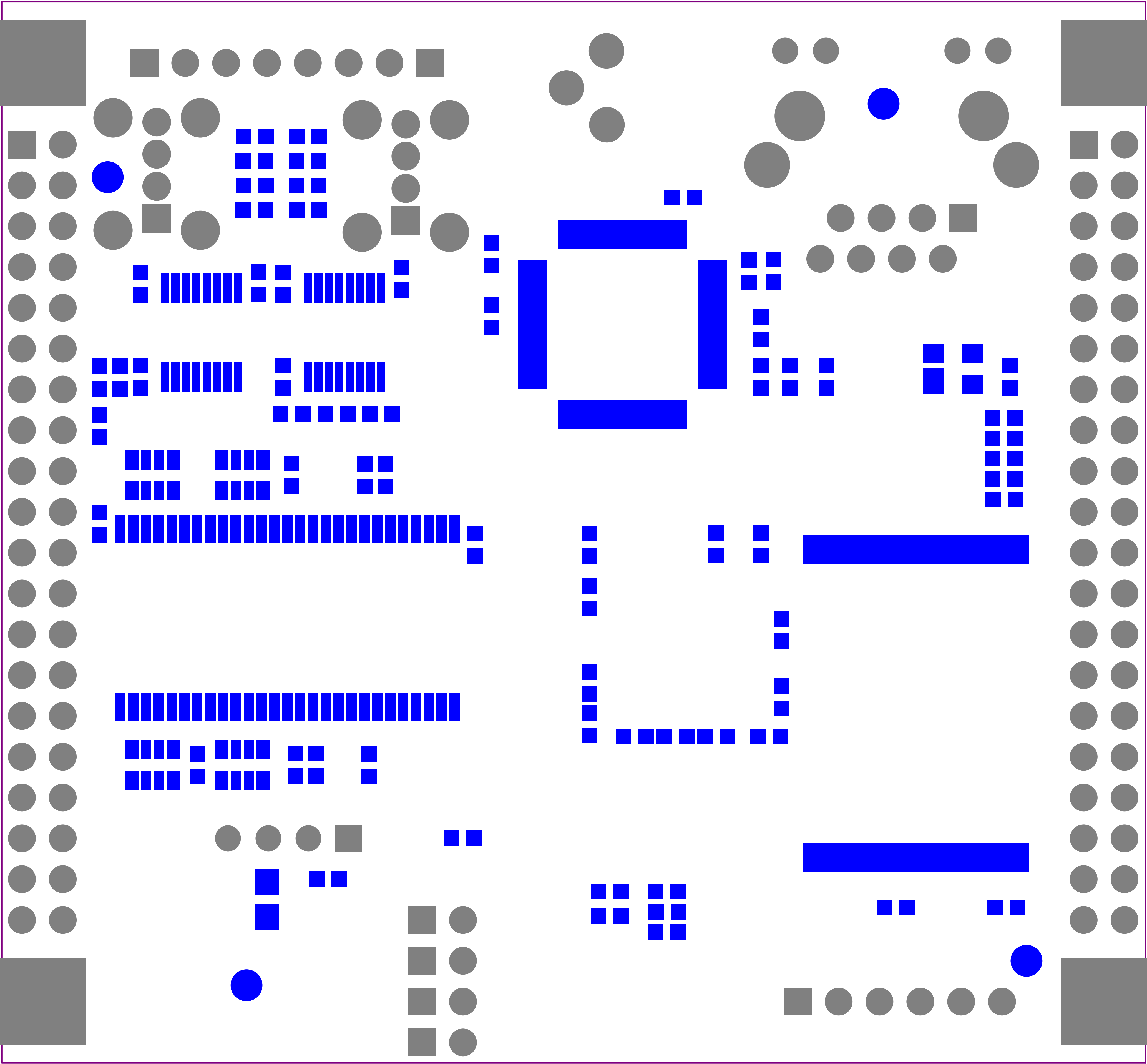


Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Mechanical 1

Top Solder



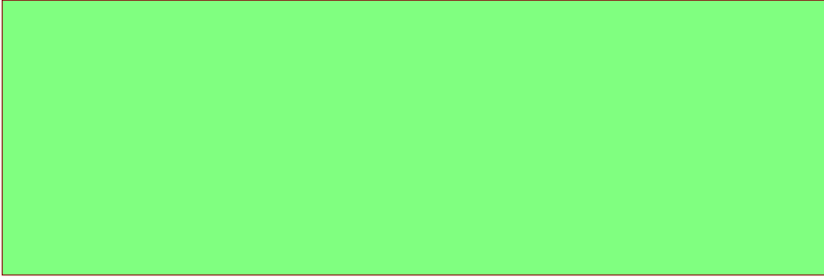
Layer Stack Up Detail for: FOX_LX.PCBDOC

Layer
Name

Mechanical 1

Bottom Solder

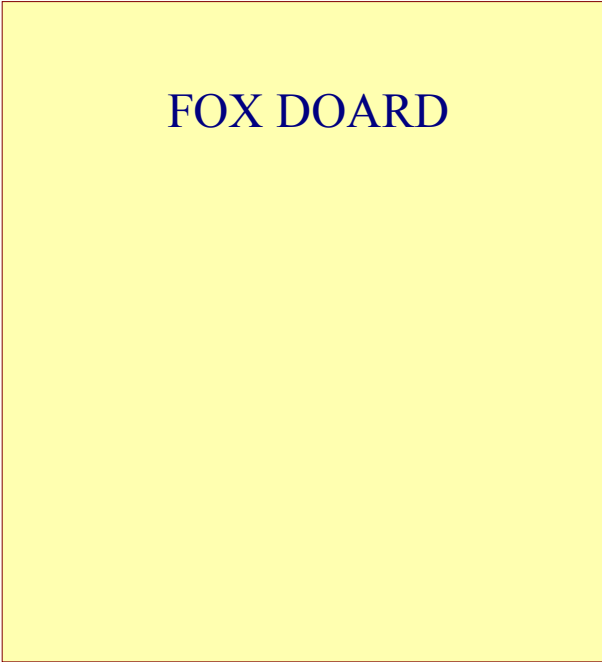
U_CPU
CPU.SCHDOC



U_ETH
ETH.SchDoc



FOX



FOX BOARD



FID1



FID2



FID3



FID4



FID5



FID6



FID7

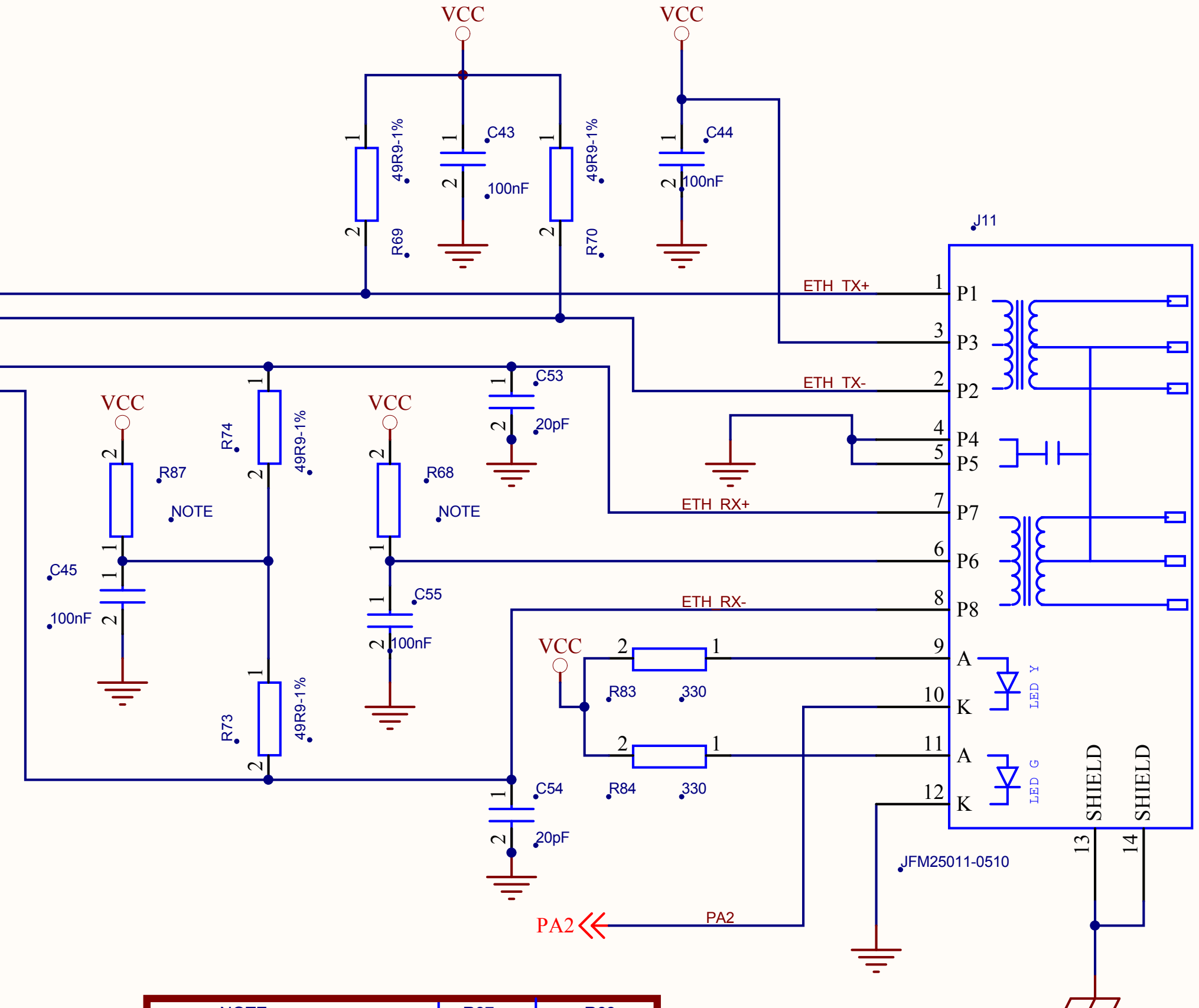
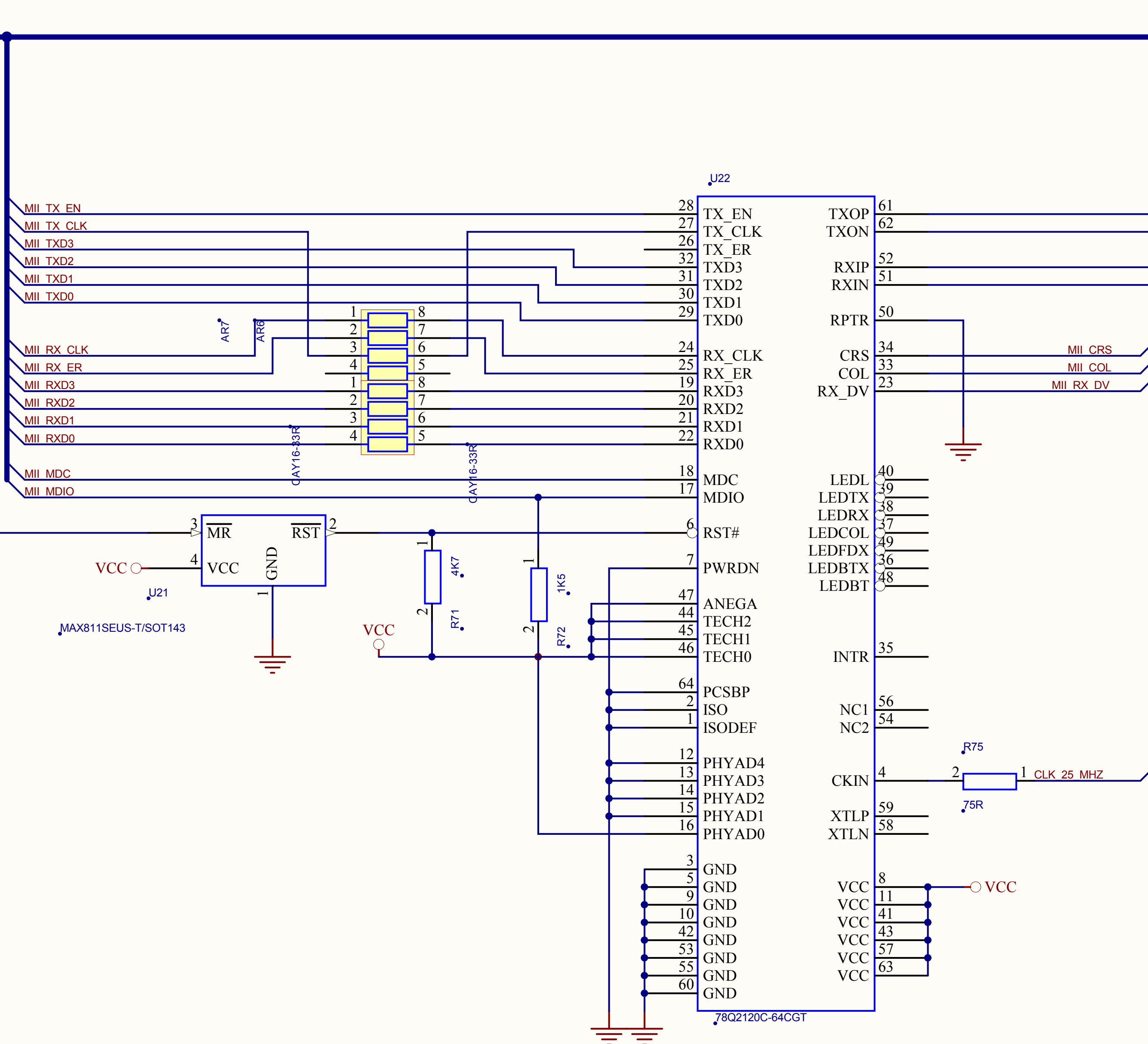


FID8

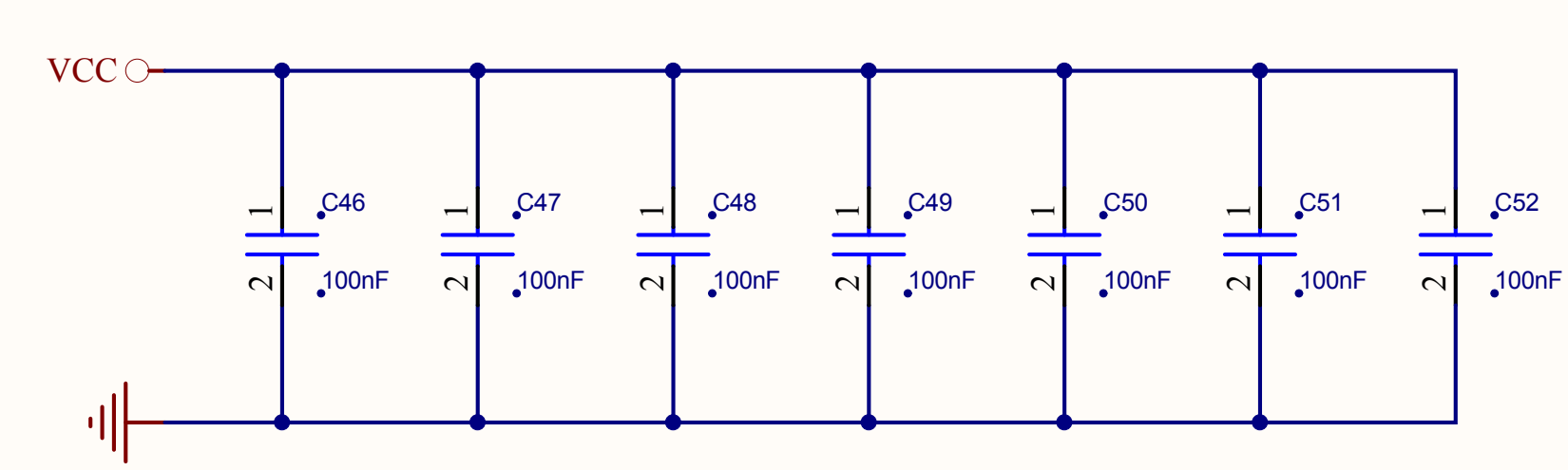
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NOTE	R87	R68
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78Q2120C08&C09	0R	0R



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